

FIG.4

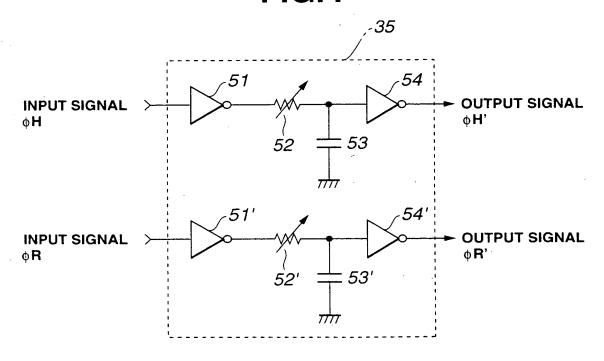


FIG.2

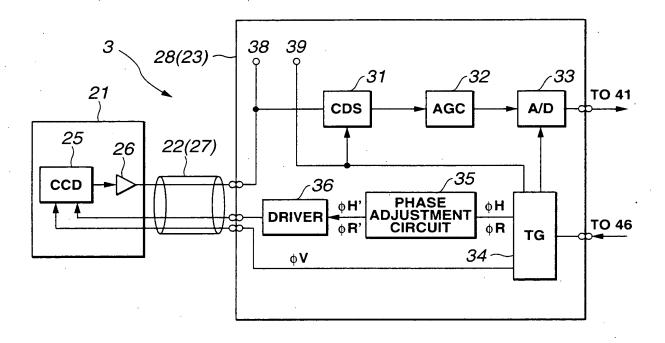
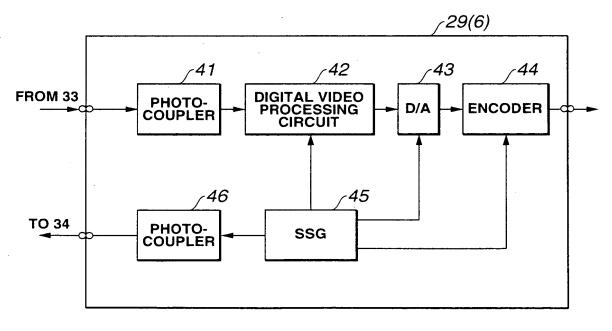
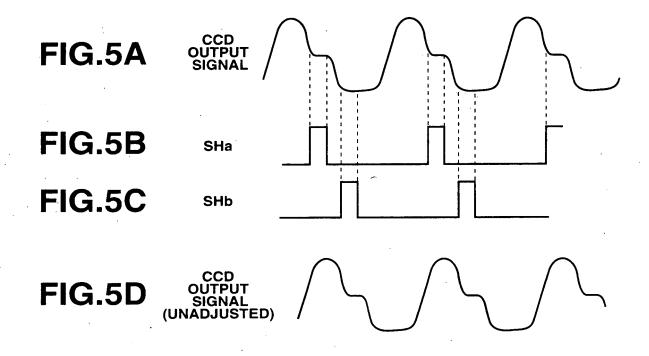
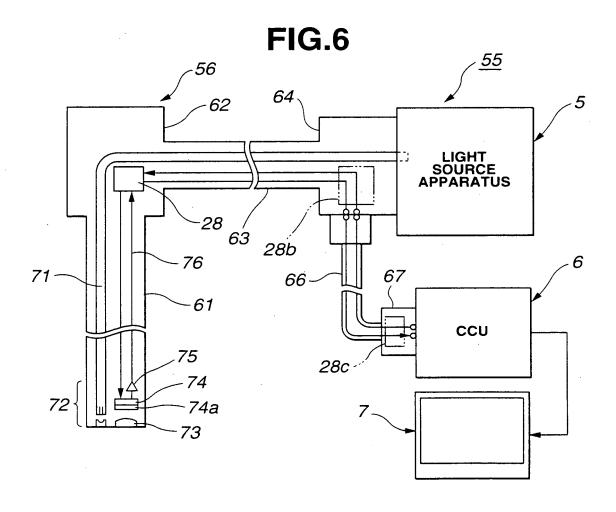


FIG.3







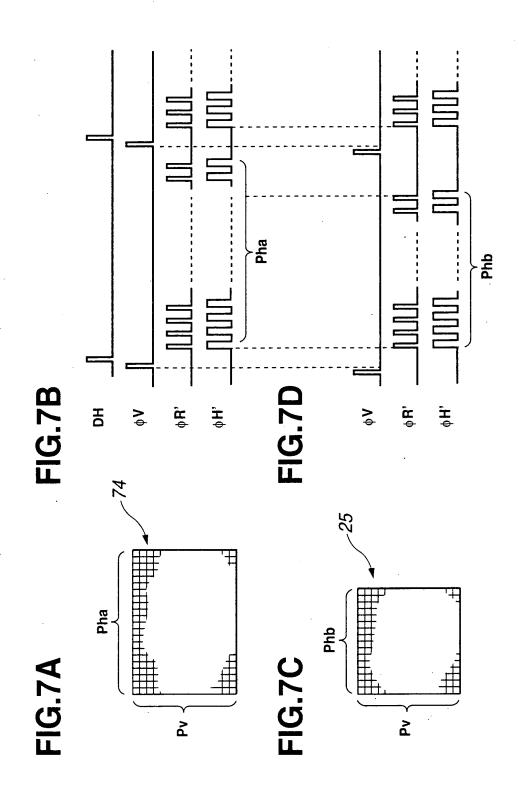


FIG.8

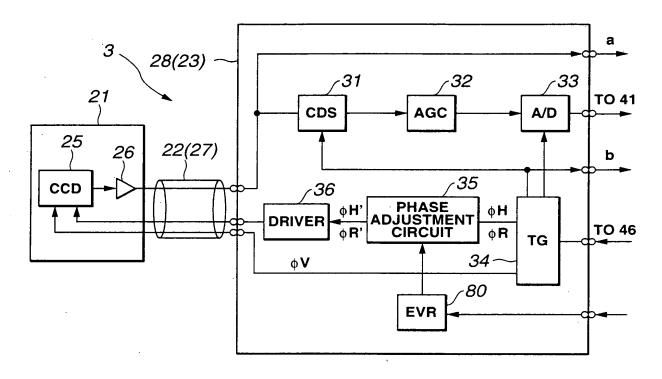
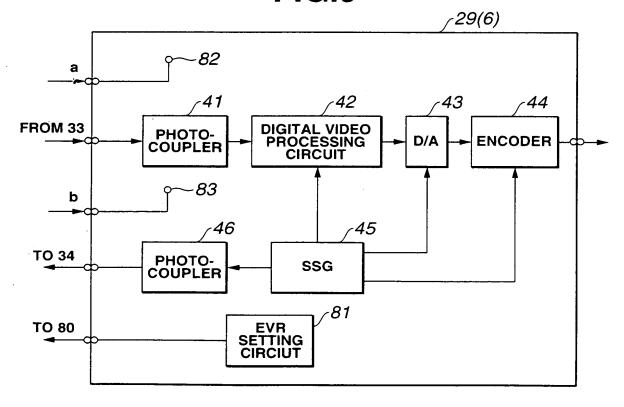
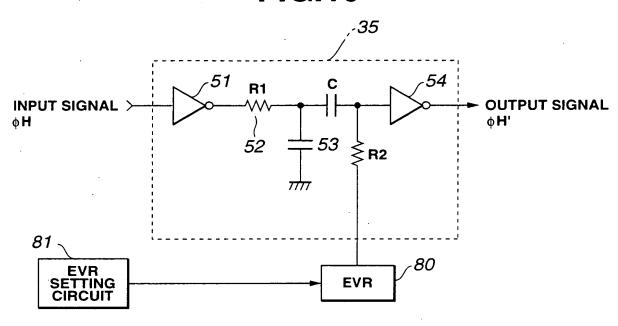


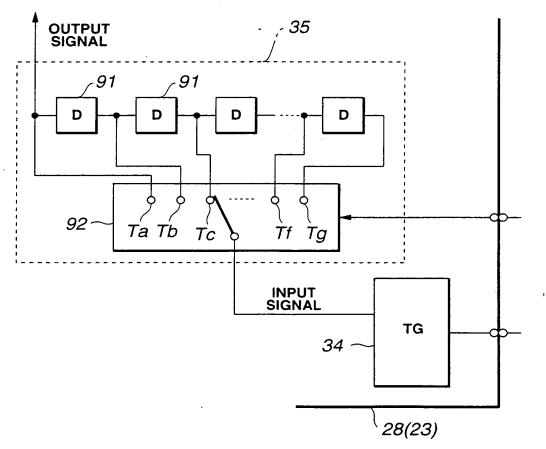
FIG.9



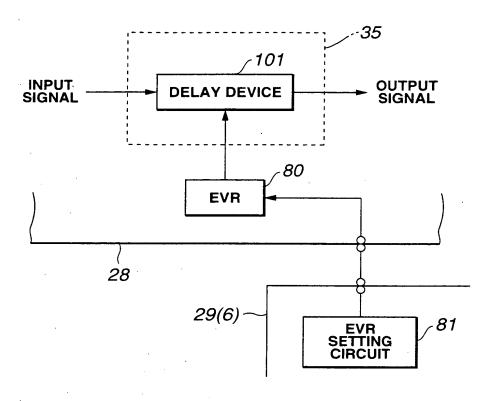
**FIG.10** 



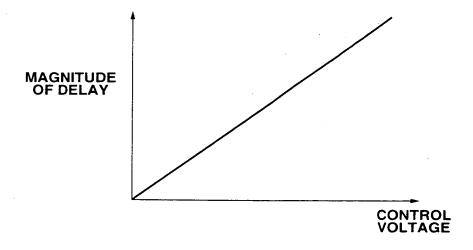
**FIG.11** 



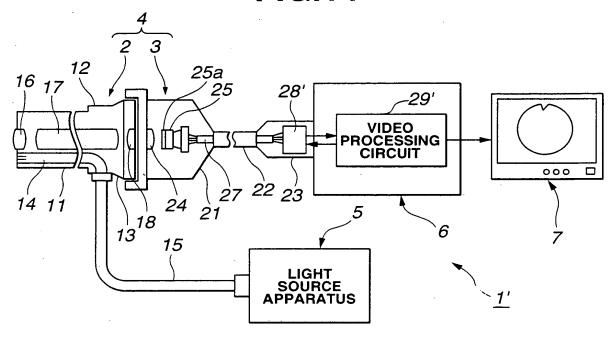
**FIG.12** 



**FIG.13** 



**FIG.14** 



**FIG.15** 

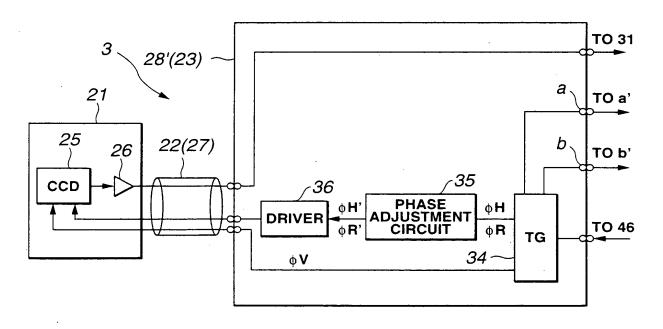
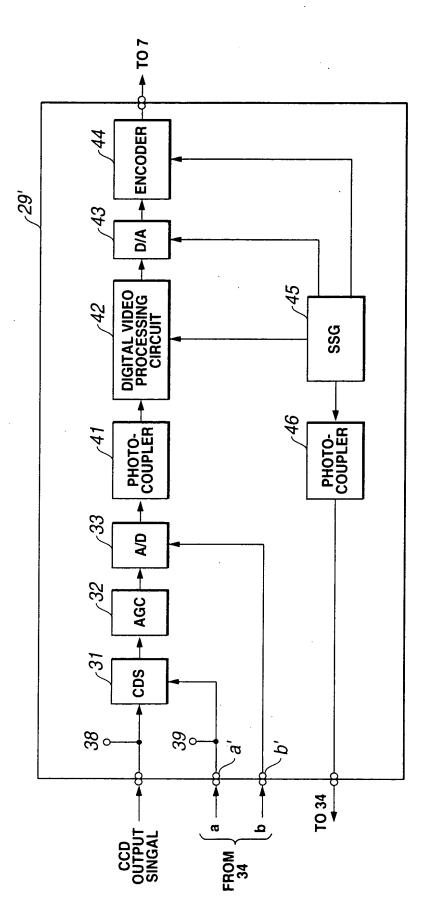


FIG. 16



**FIG.17** *55*′ 56 64 -62 LIGHT SOURCE APPARATUS -28' 63 28<sup>'</sup>b 76 -6 71 67 66 61 CCU *75* 28'c 74 72 74a 73

**FIG.19** 3 TO 31 28"(23) 21 а TO a' 25 b 22(27) 26 TO b' -35 CCD -36 PHASÉ ADJUSTMENT CIRCUIT φH' φН FROM 46 DRIVER φR φR TG 34 φV 80 FROM 81 **EVR** 

**FIG.18** 

